

TIME TABLE FOR M.E./M.TECH. DEGREE EXAMINATIONS -NOV/DEC-2014

for candidates admitted in Anna University, Chennai in 2009 & 2012

erstwhile Anna University of Technology, Chennai in 2010 and

all erstwhile Anna Universities of Technology in 2011

Regulations : 2009

Branch:M.E-VLSI Design

I Semester		
	Session :	10 A.M. to 1 P.M.
Exam Date	Day	Sub.Code/Name
17/11/2014	Monday	MA9217:Applied Mathematics for Electronics Engineers
29/11/2014	Saturday	VL9212:VLSI Design Techniques
08/12/2014	Monday	VL9211:DSP Integrated Circuits
11/12/2014	Thursday	AP9212:Advanced Digital System Design
15/12/2014	Monday	VL9213:Solid State Device Modeling and Simulation
II Semester		
	Session :	10 A.M. to 1 P.M.
Exam Date	Day	Sub.Code/Name
14/11/2014	Friday	AP9222:Computer Architecture and Parallel Processing
18/11/2014	Tuesday	AP9224:Embedded Systems
02/12/2014	Tuesday	AP9221:Analysis and Design of Analog Integrated Circuits
10/12/2014	Wednesday	VL9221:CAD for VLSI Circuits
Electives		
	Session :	10 A.M. to 1 P.M.
Exam Date	Day	Sub.Code/Name
10/11/2014	Monday	VL9253:VLSI Signal Processing
11/11/2014	Tuesday	VL9255:Design of Semiconductor Memories
12/11/2014	Wednesday	VL9256:VLSI Technology
15/11/2014	Saturday	VL9254:Analog VLSI Design
21/11/2014	Friday	VL9265:DSP Processor Architecture and programming
24/11/2014	Monday	AP9252:Neural Networks and Its Applications
25/11/2014	Tuesday	VL9257:Physical Design of VLSI Circuits
26/11/2014	Wednesday	VL9261:ASIC Design
27/11/2014	Thursday	AP9256:Electromagnetic Interference and Compatibility in System Design
28/11/2014	Friday	VL9264:Digital Speech Signal Processing
04/12/2014	Thursday	VL9252:Low Power VLSI Design
05/12/2014	Friday	VL9266:Introduction to MEMS System Design
09/12/2014	Tuesday	MX9011:Bio MEMS
12/12/2014	Friday	AP9213:Advanced Microprocessors and Microcontrollers
13/12/2014	Saturday	VL9251:Testing of VLSI Circuits
17/12/2014	Wednesday	VL9258:Genetic Algorithms and their Applications